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8. The semiconductor device component of claim 1, wherein said at least one stabilizer has a cross-sectional plan of one of quadrilateral, round, oval, and triangular.

9. The semiconductor device component of claim 1, wherein said at least one stabilizer is elongated in a direction parallel to said active surface.

10. The semiconductor device component of claim 1, further comprising protruding conductive structures in contact with selected ones of said contact pads.

11. The semiconductor device component of claim 10, wherein said conductive structures comprise at least one of solder bumps, conductive columns, conductor-filled columns, and z-axis conductive adhesive.

12. The semiconductor device component of claim 1, wherein said substrate comprises a semiconductor wafer with a plurality of dice thereon.

13. A semiconductor device component, comprising:
a substrate having an active surface with contact pads exposed thereto, said contact pads being configured to be connected with conductors on a first surface of another semiconductor device; and
at least one stabilizer protruding from said active surface and positioned between a periphery of said active surface and said contact pads, said at least one stabilizer having a plurality of superimposed, contiguous, mutually adhered layers.

14. The semiconductor device component of claim 13, wherein said at least one stabilizer protrudes from said active surface a distance no more than a distance that at least one conductive structure to be disposed in contact with at least one of said contact pads will extend beyond said active surface.

24. The semiconductor device component of claim 13, wherein said at least one stabilizer maintains a substantially uniform distance between said active surface and said first surface of another semiconductor device.

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25. A semiconductor device component, comprising:
a substrate having an active surface with contact pads exposed thereto, said contact pads being configured to be connected with conductors on a first surface of another semiconductor device; and
at least one stabilizer protruding from said active surface and positioned between a periphery of said active surface and said contact pads, said at least one stabilizer configured to allow an insulative underfill material to flow into a space created when said semiconductor device is connected with said another semiconductor device.

26. The semiconductor device component of claim 25, wherein said stabilizer is configured so that voids do not occur in said underfill material when said underfill material is flowed into said space created when said semiconductor device is connected with said another semiconductor device.

27. The semiconductor device component of claim 25, wherein said at least one stabilizer protrudes from said active surface a distance no more than a distance that at least one conductive structure to be disposed in contact with at least one of said contact pads will extend beyond said active surface.

28. The semiconductor device component of claim 27, wherein said at least one stabilizer protrudes from said active surface a distance that permits conductive structures on said contact pads to contact said conductors of said another semiconductor device.

29. The semiconductor device component of claim 25, wherein said at least one stabilizer comprises a dielectric material.

30. The semiconductor device component of claim 25, wherein said at least one stabilizer comprises a photocurable material.

31. A semiconductor device component, comprising:
a substrate having an active surface with contact pads exposed thereto, said contact pads being configured to be connected with conductors on a first surface of another semiconductor device; and
at least one stabilizer protruding from said active surface and positioned between a periphery of said active surface and said contact pads, said at least one stabilizer fabricated directly on said active surface of said substrate.

32. A semiconductor device component, comprising:
a substrate having an active surface with contact pads exposed thereto, said contact pads being configured to be connected with conductors on a first surface of another semiconductor device; and
at least one stabilizer protruding from said active surface and positioned between a periphery of said active surface and said contact pads, said at least one stabilizer preformed separately from said substrate and subsequently attached to said active surface of said substrate.

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